FOCF20 58576200

FIG. 1

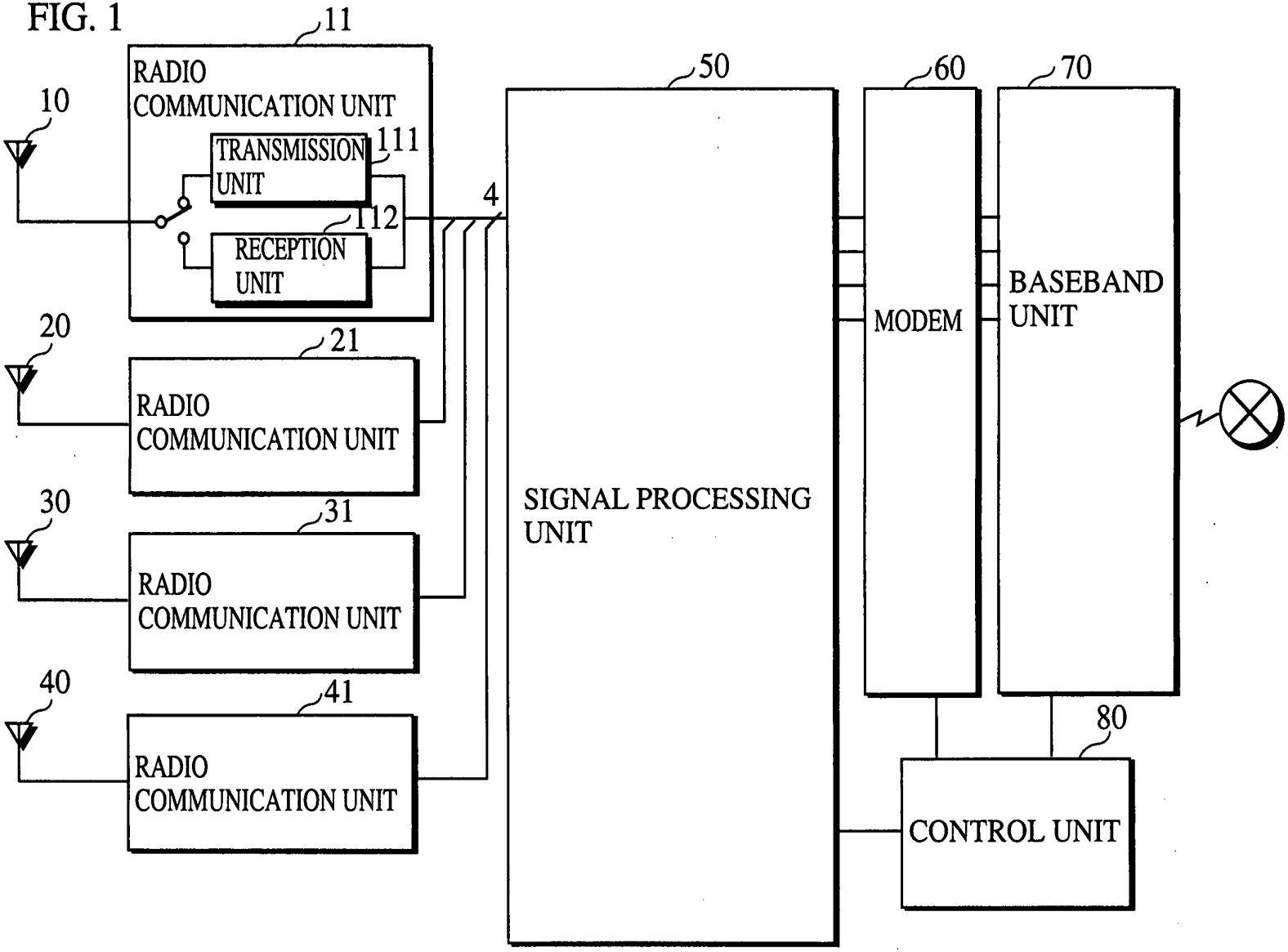
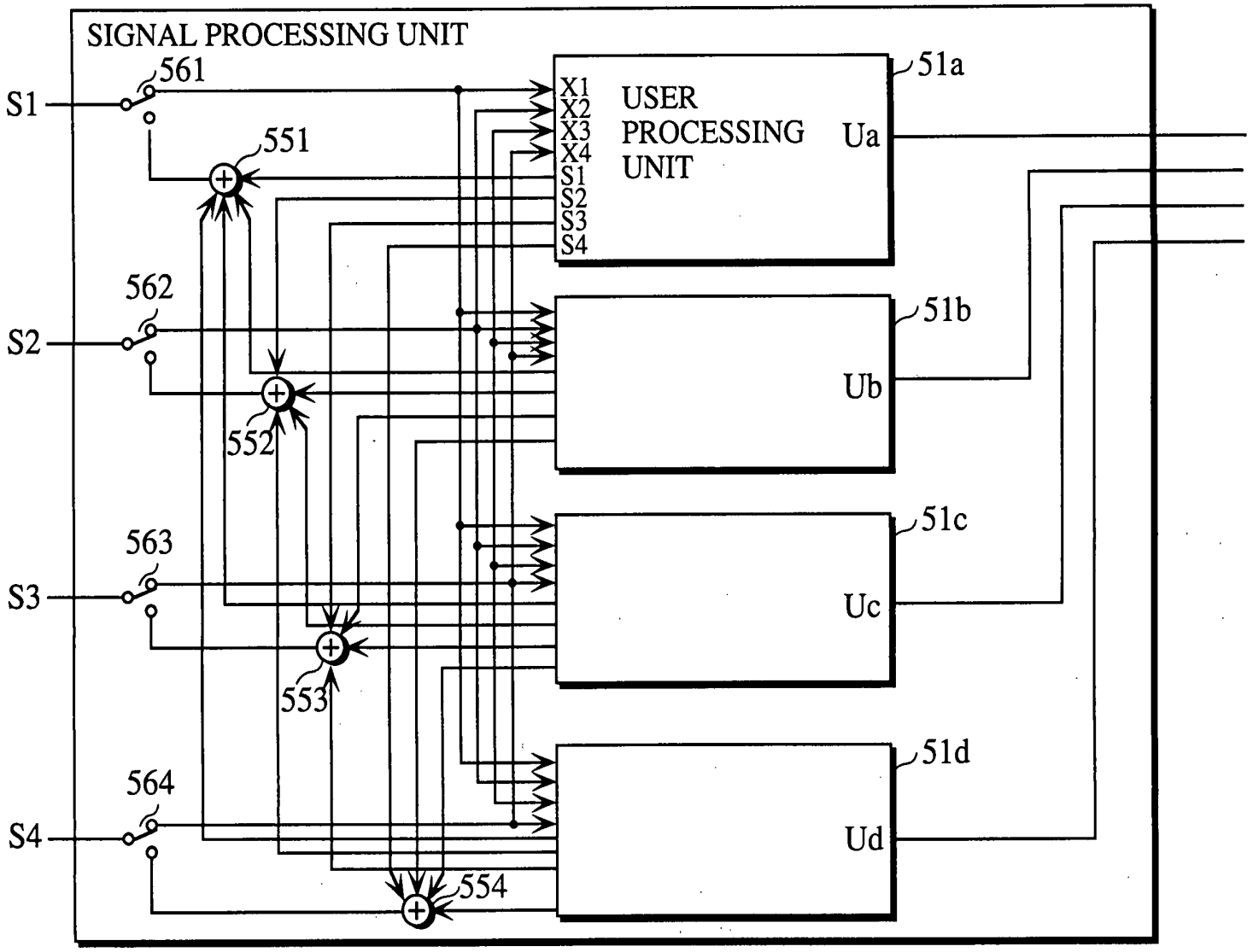


FIG.2


SIGNAL PROCESSING UNIT
561
S1
551
562
S2
552
563
S3
553
564
S4
554
X1
X2
X3
X4
S1
S2
S3
S4
USER
PROCESSING
UNIT
Ua
51a
Ub
51b
Uc
51c
Ud
51d

FOREIGN PATENT DOCUMENT

FIG.3

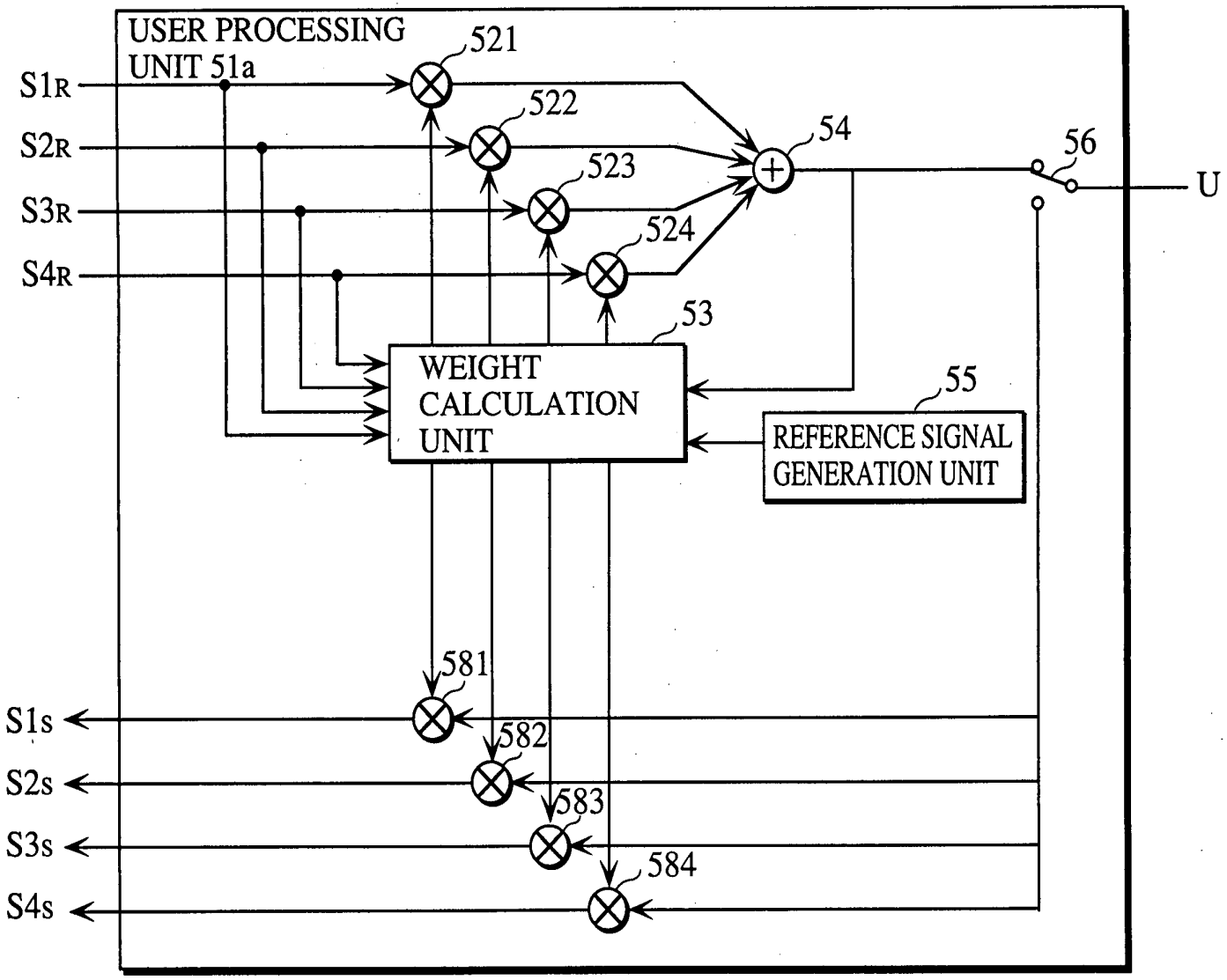


FIG. 4

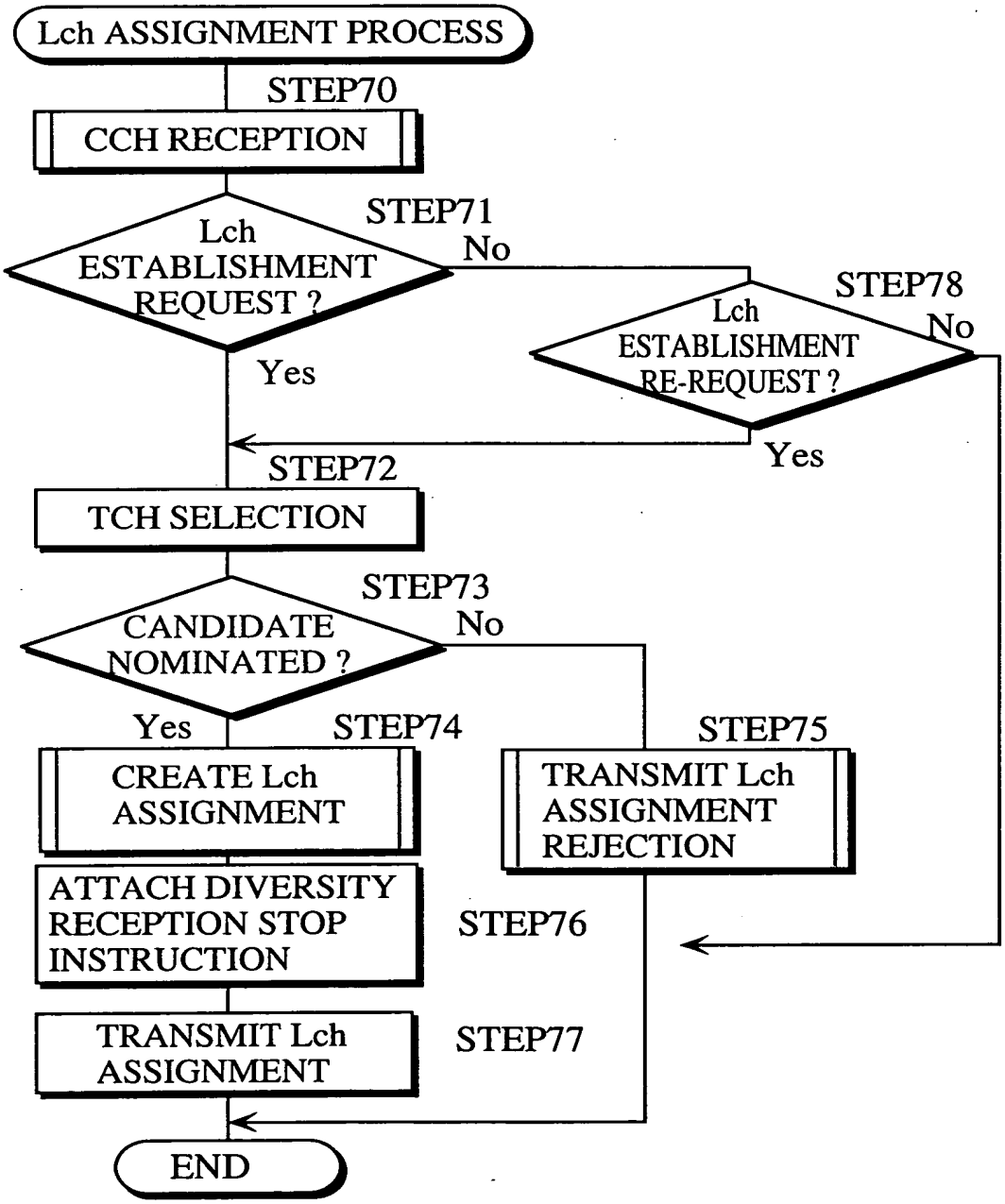

Lch ASSIGNMENT PROCESS
STEP70
CCH RECEPTION
STEP71
Lch ESTABLISHMENT REQUEST ?
No
Yes
STEP78
Lch ESTABLISHMENT RE-REQUEST ?
No
Yes
STEP72
TCH SELECTION
STEP73
CANDIDATE NOMINATED ?
No
Yes
STEP74
CREATE Lch ASSIGNMENT
STEP75
TRANSMIT Lch ASSIGNMENT REJECTION
ATTACH DIVERSITY RECEPTION STOP INSTRUCTION
STEP76
TRANSMIT Lch ASSIGNMENT
STEP77
END

FIG.5

LINK CHANNEL ASSIGNMENT MESSAGE

| OCTET \ BIT | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 |
|---|---|---|---|---|---|---|---|---|
| 1 | RESERVED | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| | | MESSAGE TYPE | | | | | | |
| 2 | LCH TYPE | | | RESERVED | | EXTENDED LCH PROTOCOL TYPE | | |
| 3 | CC PROTOCOL | | D | RELATIVE SLOT NO. | | | | |
| 4 | CARRIER NO. | | | | | | | |
| 5 | | | | | | ABSOLUTE SLOT NO. | | |

D=1;DIVERSITY RECEPTION STOP INSTRUCTION
D=0;DIVERSITY RECEPTION AVAILABLE

FIG. 6

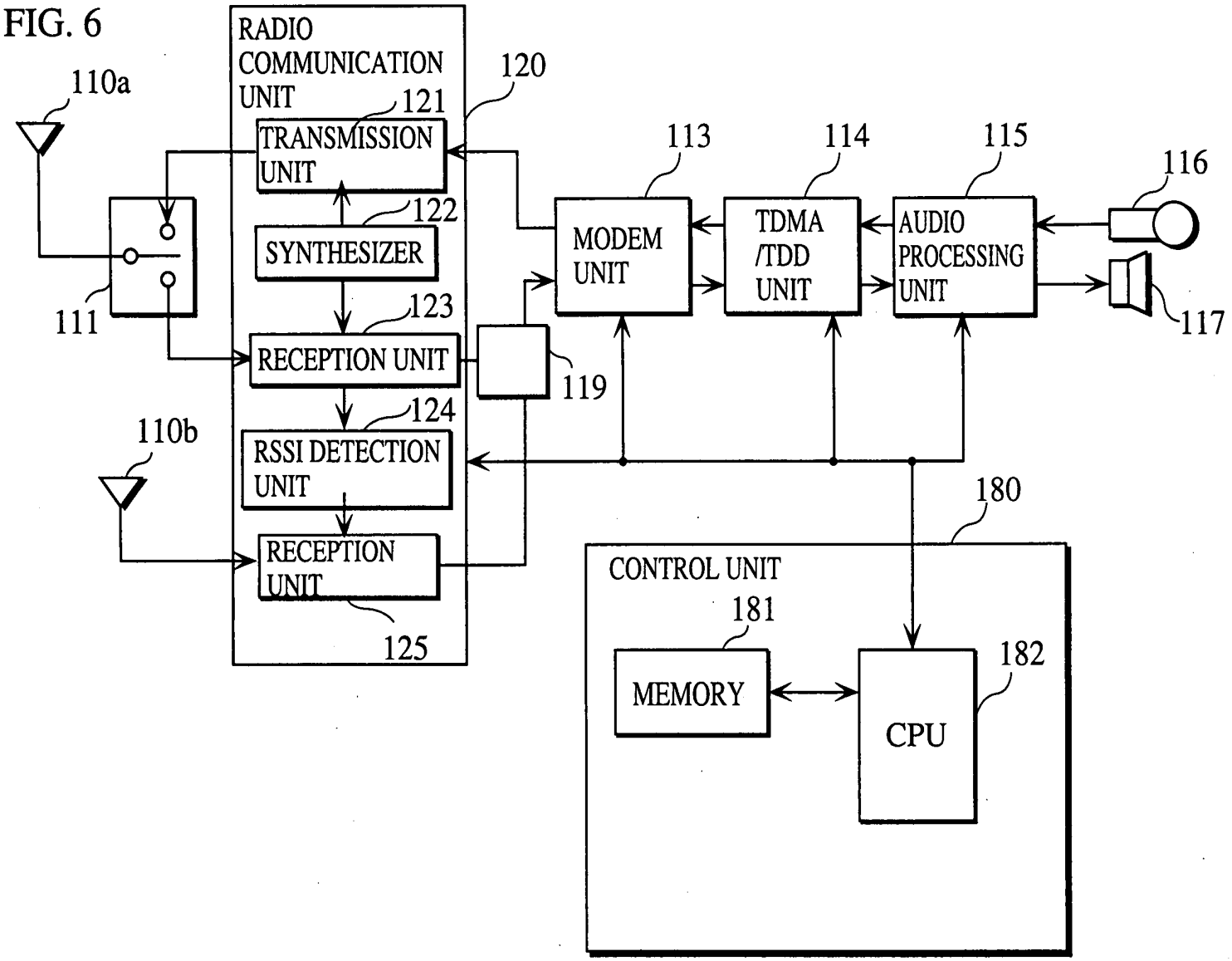

RADIO COMMUNICATION UNIT
120
110a
121
TRANSMISSION UNIT
113
114
115
116
122
SYNTHESIZER
MODEM UNIT
TDMA /TDD UNIT
AUDIO PROCESSING UNIT
111
123
RECEPTION UNIT
119
117
110b
124
RSSI DETECTION UNIT
180
RECEPTION UNIT
CONTROL UNIT
181
125
MEMORY
182
CPU

FIG. 7

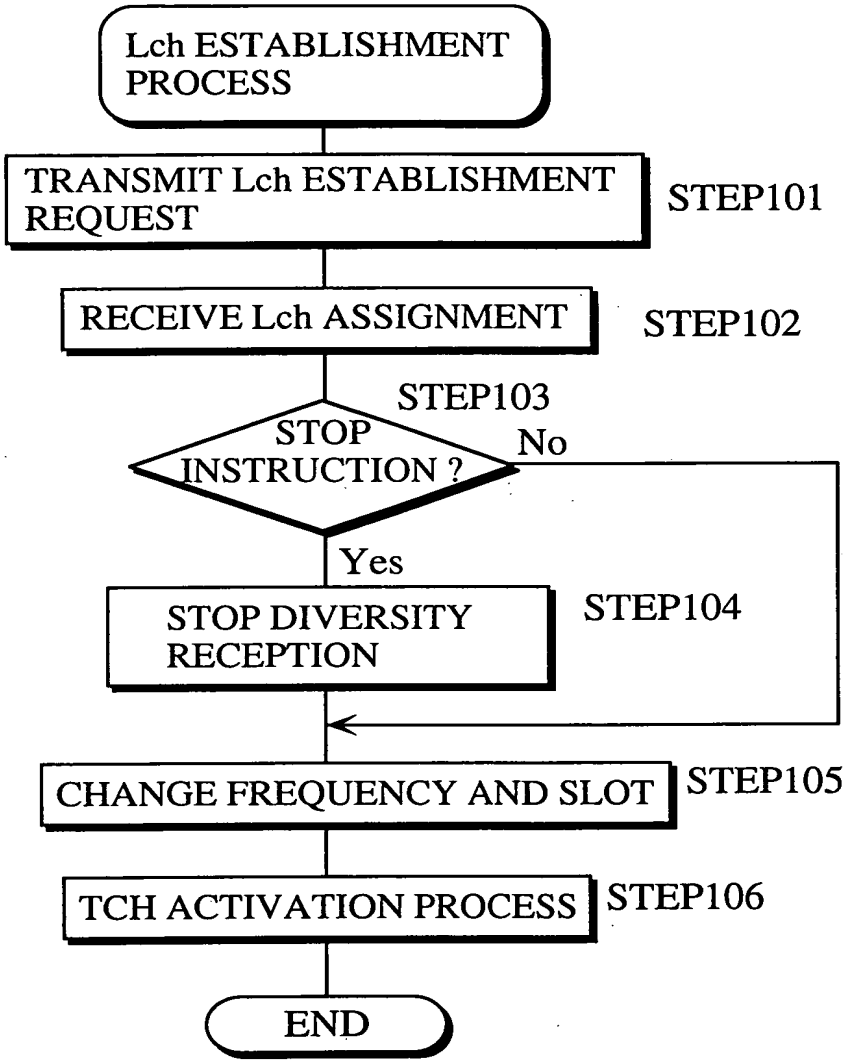

Lch ESTABLISHMENT PROCESS
TRANSMIT Lch ESTABLISHMENT REQUEST
STEP101
RECEIVE Lch ASSIGNMENT
STEP102
STEP103
STOP INSTRUCTION ?
No
Yes
STOP DIVERSITY RECEPTION
STEP104
CHANGE FREQUENCY AND SLOT
STEP105
TCH ACTIVATION PROCESS
STEP106
END

FIG. 8

| SLOT2,6 | SLOT3,7 | SLOT4,8 |
|---|---|---|
| PS1 | PS2 | PS3 |
| PS4 | — | — |
| — | — | — |
| — | — | — |

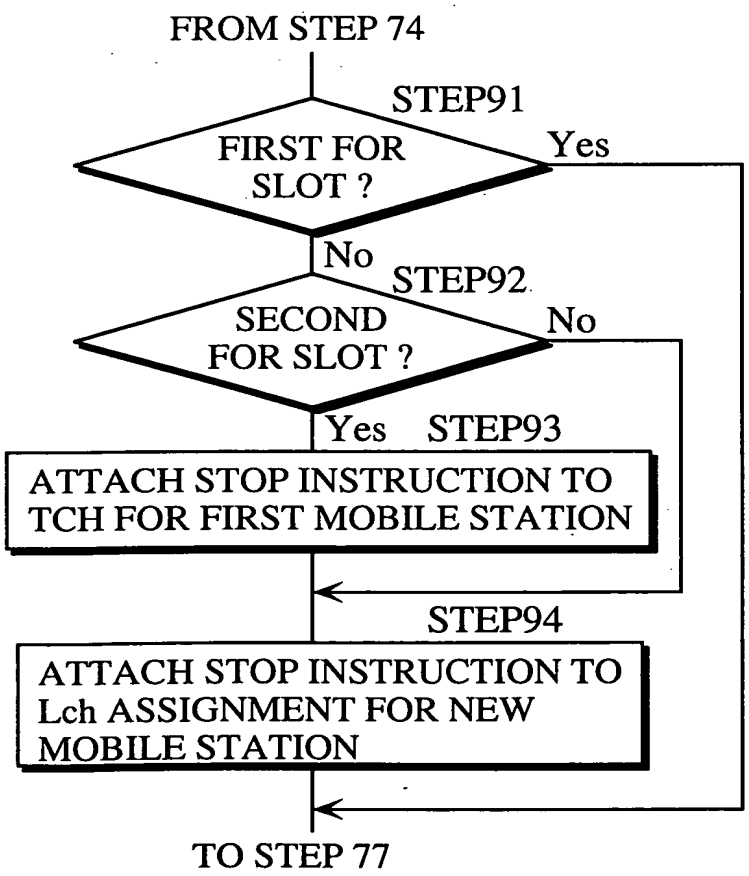



FIG. 9